

**IEEE P1800 SystemVerilog Ballot Review Committee**  
**APPROVED MINUTES**  
**3 September 2009**  
**8:00 a.m. – 10:00 a.m. PDT**  
**Teleconference**

**1 CALL TO ORDER - INTRODUCTIONS & AFFILIATIONS**

The meeting was called to order at 8:03 a.m. PDT.

**Member DRs/DRA's Present:**

Dennis Brophy (Mentor Graphics, DR)  
Mehdi Mohtashemi (Synopsys, DR)  
Karen Pieper (Accellera, DR)  
Yossi Levi (Intel DR)  
Neil Korpusik (Sun, DR)

**Member DRAs Present:**

Yatin Trivedi (Synopsys DRA)  
Matt Maidment (Intel DRA)

**Observers:**

Michiel Ligthart (Verific Design Automation)

**2 APPROVE AGENDA**

A motion (Dennis Brophy, Yatin Trivedi) was made to approve the agenda. The motion passed unanimously.

**3 PATENT POLICY AND CALL FOR PATENTS**

Chair Pieper called the group's attention to the patents slides which can be found [here](#).

**4 FUNDING, BUDGETING, AND FINANCIAL UPDATE**

This item is move to later to discuss.

**5 EDITING UPDATE**

The Editor departs on an extended leave today. Karen will get documentation source from the Editor in the event more changes are needed or to handoff to the IEEE editors if the work is complete. Since the recirculation ballot is still open, we don't know yet if there are more edits or not.

**6 ADJOURNMENT**

A motion (Dennis Brophy, Yatin Trivedi) made to suspend the meeting until 10 September 2009 at 8 a.m. PDT. The motion was approved unanimously. The meeting suspended at 8:09 a.m.