

December 5, 2004

Dear Colleague:

Following are the logistics and agenda of our next SystemVerilog work group F2F meeting.

**Date:**

Wednesday, December 15 2004, 9:00 am – 5:00 pm US Pacific Time

**The meeting will be held at:**

Mentor Graphics Corp.  
Building D - Room: To Be Assigned  
8005 SW Boeckman Rd  
Wilsonville, OR 97070-7777  
Main number: 503-685-7000  
Dennis Brophy (our host) mobile number: 503-706-8987  
Map to location:  
[http://www.mentor.com/company/office\\_locations/headquarters/directions\\_wv.cfm](http://www.mentor.com/company/office_locations/headquarters/directions_wv.cfm)

**Dial-In Instructions:**

Telephone Number: 888-742-8686  
International Number: +1-303-928-2600  
Conference ID: 8009932

Dennis Brophy has also setup a **WebEx** invitation to the group, in order to enable follow-up on any presentations being done at the meeting by those who cannot attend in person. To attend this meeting, you must first register for it. Please click the following link to see more information about and register for this meeting.

Once you have registered for the meeting, you will receive an email message confirming your registration. This message will provide the information that you need to join the meeting.

<https://mentor.webex.com/mentor/j.php?ED=82226347&RG=1>

The chair of the meeting is:

Johny Srouji  
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## Agenda

- 1) Call to order - Introductions & Affiliations
- 2) Approve agenda
- 3) Call for Patents
- 4) Approval of 22 November 2004 meeting minutes (Unapproved minutes are attached to this note)
- 5) Action Items Review and Follow-up (Action items are attached to this note)
- 6) Discussion and Review of the Patents form and outcome of PatCom meeting on December 6 - Noelle Humenick
  - a. Discussion of potential impact on the WG P&P
- 7) Status checkup on returned forms by WG members - Johny Srouji
- 8) P1800 Financial report update – Victor Berman / Oz Levia / Noelle Humenick
- 9) Status Update on members fees collection and next payments for IEEE-SA – Dennis Brophy
- 10) Status Update on 1364 Errata – open issues; approved and pending WG ratification - Tom Fitzpatrick / Neil Korpusik
- 11) Status Update of IEEE SystemVerilog LRM and Issues which were incorporated into Draft2.0 – Stu Sutherland / Karen Pieper
- 12) Status Updates from SystemVerilog technical committees and Champions sub-group – Karen Pieper
- 13) Present recently passed issues by SystemVerilog technical committees - Karen Pieper
  - a. Technical Update on Data Types resolution
  - b. Finalize resolution on issue #50 (Change DPI SystemVerilog representation to match PLI/VPI representation)
- 14) Review and Ratification of all recently passed issues which were passed by SystemVerilog technical committees
- 15) Review, Modification & Tracking of P1800 WG schedule – Johny Srouji / Noelle Humenick
  - a. Status Update on 1364 Verilog draft milestones insertion under P1800 WG schedule – Tom Fitzpatrick / Neil Korpusik
- 16) New business
- 17) Next meeting (announce date, time, and location, if known)
- 18) Adjournment

Sincerely,

Johny Srouji, P1800 Work Group Chair