

November 8, 2004

Dear Colleague:

Following are the logistics and agenda of our next SystemVerilog work group F2F meeting.

**Date:**

Monday, November 22 2004, 9:00 am – 4:00 pm Israel Time (GMT +02:00)

Note: Israel time zone is 7 hours ahead of US Eastern time zone.

**The meeting will be held at:**

Intel Corporation  
M.T.M Scientific Industries Center  
Haifa, 31015, Israel

Haifa is about 70 miles north of Tel Aviv International airport. You can either take a taxi or use the train. Please contact me in case you need further assistance regarding transportation and hotel accommodations in Haifa.

We will also have the following **bridge number:**

Teleconference details:

Dial-in: **888-742-8686**

International Dial-in: **+1-303-928-2600**

Conference ID: **8009932**

Dennis Brophy has also setup a **WebEx** invitation to the group, in order to enable follow-up on any presentations being done at the meeting by those who cannot attend in person. For those who are interested to register for this meeting, please click the following link to see more information and register for the meeting. Once you have registered for the meeting, you will receive an email message confirming your registration. This message will provide the information that you need to join the meeting.

WebEx URL: <<https://mentor.webex.com/mentor/j.php?ED=82140682&RG=1>>

The chair of the meeting is:

Johny Srouji  
Intel Corporation  
P.O. Box 1659  
Haifa 31015  
Israel  
tel: +972-4-865-5265  
mobile: +972-547-885265  
fax: +972-4-865-5999  
email: johny.srouji@intel.com

## **Agenda**

- 1) Call to order - Introductions & Affiliations
- 2) Approve agenda
- 3) Call for Patents
- 4) Approval of 11 October 2004 meeting minutes  
[Download](#) minutes from eda.org
- 5) Action Items Review and Follow-up  
[Download](#) action items from eda.org
- 6) Discussion and Review of the Patents form as supplied by PatCom. Decide on next steps – Noelle Humenick and Johny Srouji
- 7) Status Update on 1364 Verilog sub-group chair election – Johny Srouji
- 8) Status Update on Technical Editor bid; election; and contract - Noelle Humenick
- 9) Status Update and Review of IEEE SystemVerilog LRM Draft 2.0 – Technical Editor
- 10) Status Update on members fees collection – Dennis Brophy
- 11) Status Updates from SystemVerilog technical committees and Champions sub-group – Karen Pieper
- 12) Technical Update on Data Types effort under SV-BC, and that is led by Kathy McKinley
- 13) Present recently passed issues by SystemVerilog technical committees - Karen Pieper
- 14) Review and Ratification of all recently passed issues which were passed by SystemVerilog technical committees
- 15) Refresh of our WG structure; reflect officers' election; 1364 sub-WG chair election; Technical Editor election
- 16) Review & Track our schedule of our major milestones
- 17) Next Planned P1800 press releases
- 18) New business
- 19) Next meeting (announce date, time, and location, if known)
- 20) Adjournment

Sincerely,

Johny Srouji, P1800 Work Group Chair