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July 13, 2004

**Re: Letter of Assurance from Synopsys, Inc.**

Dear Johny Srouji:

On behalf of Synopsys, Inc., I hereby provide a letter of assurance with respect to patents and patent applications relevant to the IEEE P1800 SystemVerilog Working Group (hereinafter "SystemVerilog"):

Synopsys has not conducted a review of its patent portfolio to determine what patents, if any, may be relevant to implementation of the proposed SystemVerilog specification.

Synopsys may own patents or patent applications which apply to the SystemVerilog specification that is submitted to the working group. If implementation of an IEEE standard based on the SystemVerilog specification requires the use of Synopsys patents, Synopsys will grant, upon written request, a non-exclusive license under such patents on reasonable and non-discriminatory terms.

Should you have further questions, please feel free to contact me.

Very Truly Yours,

Erik L. Oliver  
Sr. IP Counsel