

SV-CC Input for next PAR

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SV-CC Enhancements

- Define interaction between VPI and DPI
- VCD dumping for new SystemVerilog constructs
 - Database Read API?
- Class based interface
 - Better interface to other languages like C++
- Define a UML model of VPI

SV-CC Enhancements

- Update interfaces to support new SystemVerilog constructs
 - Checkers
 - Virtual interfaces (section 37.15)
 - Assignment Patterns (section 37.67)
 - Covergroups and functional coverage
 - Statements used as expressions and Expressions used as statements
 - Many others

SV-CC Enhancements

- Update IP protection model
- Efficient mechanism to track value changes of aggregate objects
- Unified Coverage Interoperability Standard
- Should we do a survey?
 - At DVCon or DAC?
 - Web site where people could enter ideas

SV-CC Enhancements

- Handling unnamed objects
 - Making objects which are currently 'implementation specific' be specific
 - Unnamed scopes
 - dynamic objects
 - compilation units
 - struct member instance naming
- Review of 100+ Open Mantis Items