

Quick Refresh on 1364

1. BTF was folded into the SV-BC.
2. Encryption committee was dissolved and errata to be addressed by the SV-BC.
3. 1364 Database to be merged with the SystemVerilog database.

As for merging Verilog 1364 into SystemVerilog, there were two main points of disagreement:

1. The effort that is required to merge the documents
2. The quality of the merged document

SystemVerilog two years version discussion

The following items were discussed:

1. SystemVerilog Support
2. Bug-fixing
3. Merging of 1800 & 1364
4. Enhancements
5. Further standardization into International Organizations
6. Interoperability and integration w/ Analog Mixed Signal (AMS) and maybe VHDL

The following table is a brief summary of opinions and priorities which were voiced by members.

	Support	Errata / Local Enh.	Extensions	Merge with 1364	Interop, Integration	International Organizations	Next LRM Version	LRM UPDATES
Intel	P-1	P-2	P-3 (SVA)	P-5	AMS P-4		2 years	6 months
SUN	P-2	P-1	P-4	P3	No big need for AMS and VHDL		2 years	3 months
IBM	P-1	P-2	P-4	P-5	P-3: AMS; VHDL		2 years	6 months
SUNBURST	P-1	P-2	P-4	P-3	P-5; AMS		2 years	6 months
SUTHERLAND	P-3	P-2	P-4	P-1	P-5; AMS		2 years	2 months
SYNOPSIS	P-1	P-2	P-5	P-4	P-3 VHDL P-6 AMS		2 years	6 months
MENTOR	P-1	P-2	P-5	P-4	P-3 VHDL, SystemC		2 years	2 months
CADENCE	P-1	P-2	P-5	P-3	P-4 VHDL AMS (resolving)		2 years	2 months
FINTRONIC USA	P-2	P-1	P-4 (SVA)	P-6	P-5 Important – VHDL, SystemC		2 years	6 months
BlueSpec	P-2	P-1	P-3	P-5	P-4 SystemC		2 years	6 months
Accellera	P-1	P-2	P-5	P-4	P3 VHDL; AMS		2 years	2 months

Intel: P-3 is SVA extensions
P-5 UDP, specify, timing checks – Verilog
Fintronics – P3 verilog fixes

SUMMARY

We do not see major enhancements in the next 2-year period. We do see a need for prioritized bug-fixes and enhancements. Set a goal to complete the next IEEE SystemVerilog Standard in two years (completion could be anything from end of technical work to actual approved and published IEEE Standard).

We need an approved mechanism to make corrections and revisions for release at least every six months.