

P1800- the next 2 years 01/19/06

Neil Korpusik
Sun Microsystems

Topics – in priority order

- Errata
 - Clarifications
 - Minor enhancements
 - Merge of 1364 and 1800 LRMs
 - Verilog-ams – not used at Sun
 - VHDL – not used at Sun
- ★ Either a database of errata or twice-annual supplements produced

Focus for next 2 years

- Solidify System Verilog
 - Fix errata
 - Clarify issues raised
 - Add minor enhancements – as required
 - A prioritization process will most likely be required
 - Merge 1364 and 1800 LRMs
 - Tool divergence is much more likely if we don't do this
- AMS and VHDL efforts should be handled similar to encryption
 - Separate small group formed to handle these
 - Group disbands after work is completed
 - Solidifying System Verilog requires much more attention right now