

(last updated on September 12, 2004 - Johnny Srouji)

IEEE P1800 SystemVerilog WG Action Items - September 2004

Action Item	Owner	Capture Date	Due Date	Status	Update
If anyone knows of a patent that may be germane to the project, please inform the Chair	All IEEE 1800 Members	1-Jul-04	6-Aug-04	No patent germane to the project was submitted	CLOSED
Review Synopsys patent information given to the chair from Victor Berman (Cadence DR)	Johnny Srouji	1-Jul-04	9-Aug-04	Chair received a letter form Assurance from Synopsys DR, Oz Levia (message)	CLOSED
Follow-up on Synopsys letter of assurance regarding compliance w/ IEEE patents policy	Johnny Srouji	26-Jul-04	9-Aug-04	in progress (message)	CLOSED
Determine how a tie is broken	Steve Mills	1-Jul-04	9-Aug-04	Done (message)	CLOSED
Make these changes to the P&Ps and get ratification of the P&Ps from the sponsors	Johnny Srouji	1-Jul-04	9-Aug-04	Done (revised P&P was approved and sent to p1800 reflector)	CLOSED
Attach a copy of the Accellera Assignment Letter to the minutes	Dennis Brophy	1-Jul-04	6-Jul-04	Done	CLOSED
Include a copy of the unapproved CAG meeting motion	Dennis Brophy	1-Jul-04	6-Aug-04	Done - Sent to Johnny on 8/8/04 (message).	CLOSED
Dennis to work with Steve on access to the unapproved SAB meeting minutes so they can become a part of this report	Dennis Brophy	1-Jul-04	9-Aug-04	Done. Draft 01 is posted at p1800 website.	CLOSED
Dennis to contact Accellera to have them tender the SystemVerilog 3.1a specification in approved and mandated IEEE format	Dennis Brophy	1-Jul-04	9-Aug-04	Done	CLOSED
Dennis to get next WG meeting logistics (of 8/9/04) details from Oz (Synopsys DR)	Dennis Brophy	1-Jul-04	9-Jul-04	Done	CLOSED
Dennis to get next WG meeting logistics (of 9/13/04) details from Wolfgang (Infineon DR)	Dennis Brophy	1-Jul-04	9-Aug-04	Email request sent to Wolfgang on 8/8.	CLOSED

Dennis to get next WG meeting logistics (of 10/11/04) details from Victor (Cadence DR)	Dennis Brophy	1-Jul-04	9-Aug-04	Email request sent to Victor on 8/8.	PENDING
Dennis to contact JEITA SV-TC Chair, Hamaguchi-san to see if they can support a SystemVerilog Working Group meeting in Japan.	Dennis Brophy	1-Jul-04	TBD	Email request sent to Hamaguchi-san on 8/8. Message reply attached. January 26, 2005 was proposed.	PENDING
Johny to send Dennis the proposed contract from Edward to be entered as part of the working group's record	Johny Srouji	1-Jul-04	6-Jul-04	Done	CLOSED
Go to Accellera and present the funding proposals from the IEEE for P1800 and the desire to have Accellera be the funding agent	Johny Srouji / Vassilios Gerousis	1-Jul-04	14-Sep-04	Vassilios presented the project requirements to the Accellera board and the request as sponsor of SystemVerilog to cover about 50% of the project costs was placed in front of the Accellera board. This \$60K request was approved.	PENDING
Get errata entered into SystemVerilog webpage located on eda.org by 15 July 2004 to help determine the funding needs	All IEEE 1800 Members	1-Jul-04	15-Jul-04	Done	CLOSED
Initiate election of Errata Technical Sub-Working Group Chair	Johny Srouji	1-Jul-04	10-Jul-04	Done	CLOSED

Captured in the meeting of August 9, 2004

Follow-up on Synopsys revised letter of assurance regarding compliance w/ IEEE patents policy	Johny Srouji	9-Aug-04	14-Sep-04	Chair received a new letter of Assurance from Synopsys following PatCom template	PENDING
Contact PatCom for advice and review of this issue for seek a format of an assurance letter that we should use	Johny Srouji	9-Aug-04	14-Sep-04	Chair contacted PatCom and received proper forms	CLOSED
Issue Letters of Assurance in keeping with prescribe patent policy at next	All IEEE 1800 Members	9-Aug-04	14-Sep-04	Open	PENDING
Determine how we keep IEEE-SA confidential information for the record	Dennis Brophy	9-Aug-04	14-Sep-04	Open	CLOSED

From sv-ec: Johnny - needs to clarify that clause a. of the IEEE patent policy applies to the work of P1800	Johnny Srouji	1-Sep-04	14-Sep-04	Open	CLOSED
Work with IEEE-SA to include information in the agreement that allows us to track time/cost usage and reports on a monthly basis	Oz Levia, Victor Berman	9-Aug-04	14-Sep-04	Done (message attached)	CLOSED
Request \$60K to cover partial cost of IEEE Funded Services program and have Accellera act as administrator for the collection of all other fees	Vassilios Gerousis]	9-Aug-04	14-Sep-04	Vassilios presented the project requirements to the Accellera board and the request as sponsor of SystemVerilog to cover about 50% of the project costs was placed in front of the Accellera board. This \$60K request was approved.	CLOSED
Define the process to get errata moved, addressed and approved by the SV WG. A method for those who do not have access to the database needs to also be put into place.	Karen Pieper	9-Aug-04	14-Sep-04	Open	CLOSED
Errata to be considered for inclusion in the standard must be submitted to the SVDB (http://www.eda.org/svdb/) by 1 September 2004	All IEEE 1800 Members	9-Aug-04	14-Sep-04	Open	CLOSED
To produce a cataloged and prioritized list of errata and report this to the SV WG at it next meeting	Karen Pieper	9-Aug-04	14-Sep-04	Open	PENDING
Identify people that could perform the role of the LRM Manager and DR tracker. Those people will be contacted to see if they would accept the role	Johnny Srouji	9-Aug-04	14-Sep-04	Open	PENDING

Identify a small ad-hoc group that would report back to the SV WG on the future alignment of 1364 with SystemVerilog. Victor will lead and Cliff, Alec and Karen along with the BTF and PTF chair and co-chairs will participate to create the report. The report will be presented to the SV WG at the October meeting. Data types issues should be filed to ensure they are addressed	Johny Srouji	9-Aug-04	14-Sep-04	Open	CLOSED
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