

IEEE P1800 SystemVerilog Working Group
APPROVED MINUTES
14 September 2004
9:00 a.m. – 5:00 p.m. CSET
Hilton Mainz, Mainz, Germany

MEMBERS PRESENT	MEMBERS ABSENT	GUEST	STAFF
Accellera		Stu Sutherland	
Blue Spec		Alec Stanculescu	
Cadence Design Systems		Jonathan Bradford	
Jeda Technologies			
IBM			
Infineon			
Intel			
Mentor Graphics			
Sun			
Sunburst Design			
Synopsys			
Verisity			

1 CALL TO ORDER

Chair Srouji called the meeting to order 9:10 am

Introductions and DR/DRA status, affiliations and guest status was determined.

<u>Name</u>	<u>Membership Status</u>	<u>Affiliation</u>
Vassilios Gerousis	DR	Accellera
Rishiyur Nikhil	DR	Blue Spec
Victor Berman	DR	Cadence Design Systems
Yossi Lichtenstein	DRA	IBM
Wolfgang Ecker	DR	Infineon
Thomas Steininger	DRA	Infineon
Johny Srouji	DR	Intel
Eugene Zhang	DR	Jeda Technologies
Dennis Brophy	DR	Mentor Graphics
Shrenik Mehta	DR	Sun Microsystems
Neil Korpusik	DRA	Sun Microsystems
Cliff Cummings	DR	Sunburst Design
Oz Levia	DR	Synopsys
Karen Pieper	DRA	Synopsys
Lori Kate Smith	DRA	Verisity
Alec Stanculescu	Guest	Fintronic USA
Jonathan Bradford	Guest	MICRONAS GmbH
Stu Sutherland	Guest	Sutherland HDL, Inc.

2 APPROVAL OF AGENDA

A motion (Oz Levia, Victor Berman) was made to approve the agenda. The motion was unanimously.

3 CALL FOR PATENTS

IEEE Patent slides were read and presented to the team by Secretary Brophy. (See <http://standards.ieee.org/board/pat/pat-slideset.ppt>)

4 APPROVAL OF 9 AUGUST 2004 MEETING MINUTES

A motion (Oz Levia, Rishiyur Nikhil) was made to approve the 9 August 2004 meeting minutes. The motion was unanimously approved.

5 ACTION ITEM REVIEW AND FOLLOW-UP

See <http://www.eda.org/sv-ieee1800/Meetings/2004/September/ActionItems.pdf> for action item review and status.

LOA ACTION REQUEST DISCUSSION

The group noted that the IEEE-SA LOA is missing a checkbox if any team wanted to follow clause 6.a of the IEEE Patent Policy. The IEEE-SA PatCom group has been contacted and they will review the need to extend their form. There was suggested language for this presented by Cadence. Oz, proposed that all just fill out the current LOA from IEEE and in parallel we will work with them to change their form or change our policies.

A motion (Vassilios Gerousis, Eugene Zhang) was made to have all P1800 DR members submit a signed IEEE LOA within two weeks or explain why the IEEE LOA cannot be signed. The motion was unanimously approved.

Action Request: [ALL] Submit a signed IEEE LOA or a reason explaining why the IEEE LOA cannot be signed by 1 October 2004.

LOA: See <http://www.eda.org/sv-ieee1800/Meetings/2004/September/loa.pdf>

Action Request: [Victor Berman, Ed Rashba] Work with PatCom to revise their LOA form to permit it to comply with clause 6. a. Victor to seek clarification on the need to read the IEEE Patent Slide information for clause 6 b when it does not apply in this case. Victor will report back within two days after the next PatCom meeting on the status of this.

6 REVIEW AND RATIFICATION OF IEEE FUNDED SERVICES PROPOSAL

Oz and Victor reported out on their conversations with the IEEE.

Confidentiality: The proposal is confidential as are all IEEE proposals. However, it is OK to share it with Accellera board (everyone on the board). If 1800 has a member's area of the website, the IEEE is OK with the proposal in posted in this area. At this point they prefer it is not distributed freely at large until they can address that question. I think that since we can share with Accellera, the main issue is largely resolved. We will have to work out how to indicate to the members of 1800 what proposal they voted for in the last meeting. We can distribute by email to members - but we cannot just post on the web site, or include in the minutes, just yet.

Cost: the Cost is as quoted. With several exceptions, one being that the editor job will be put to bid and the IEEE and the 1800 WG can select the editor from the bids. So in a sense, we will be participating in the process and approving the selection. If we select a higher cost we will have to pay for it, if we select a lower cost – our total cost will also be lower. This is the only case where the proposal is ‘estimated’ and all other figures are actual cost. The second exception is that the project is quoted for 12 month. In the event that the project takes longer, the IEEE has provided a schedule (at the end of the proposal) for overage charges. In addition (for completion sake) we should note that the amounts quoted are for specific quantity of services (i.e. number of press release, number of trips, etc). Should we use more services, IEEE has also provided a schedule (again, at the end of the proposal) that outlines the cost associated with each such services. There is not a provision for refunds. Right now, I don’t see a reason to assume we will need more services.

Monitoring: IEEE agrees to provide a monthly report to the project-monitoring group (Victor and Oz). We asked for once every two weeks, but I think this is just fine.

7 REVIEW FUNDING RESOURCES STATUS

a. Accellera Update

Accellera has agreed as sponsor to cover the requested cost and act as collection agent for the SV WG dues to cover the balance of the IEEE funded services. Accellera will be a signer to the IEEE Funded Services contract along with the WG.

Action Request: [Project Leaders] Work with Accellera to collect the project dues. Tell Accellera exactly how much to invoice by 1 October 2004.

Action Request: [Johny Srouji] Check with Intel legal on signing IEEE contract on behalf of P1800 by 1 October 2004.

Action Request: [Project Leaders] Work with IEEE to close on contract and present it to Accellera for signing by 1 October 2004.

b. Member Updates

SV WG members will wait for Accellera invoices to pay the fees. No further update.

8 REVIEW AND RATIFICATION OF ERRATA TECHNICAL SUB-WORKING GROUP OPERATING GUIDELINES

Karen Pieper reviewed the operating guidelines that had been accepted by the Errata Technical Sub-Working Group. Guidelines: See [http://www.eda.org/sv-ieee1800/Meetings/2004/September/Errata-TSWG-Operating Guidelines.pdf](http://www.eda.org/sv-ieee1800/Meetings/2004/September/Errata-TSWG-Operating%20Guidelines.pdf)

A motion (Victor Berman, Vassilios Gerousis) was made to ratify the Errata Technical Sub-Working Group Operating Guidelines for the SV-AC, SV-BC, SV-CC, and SV-EC Committees. The motion was unanimously approved.

There was discussion on use-model errata and incompatibilities in SystemVerilog and how they should be addressed. The operating guidelines clearly permit use-model errata and incompatibilities to be addressed and corrected by the Errata Technical Sub-Working Group.

9 REVIEW AND RATIFICATION OF ERRATA TECHNICAL SUB-WORKING GROUP STRUCTURE

Karen Pieper presented the proposed structure and chair/co-chair composition and champions identified to assist in addressing and closing errata. (Group structure diagram to be supplied shortly.)

A motion (Victor Berman, Rishiyur Nikhil) was made to ratify the System Verilog Errata Technical Sub-Working Group. The motion was unanimously approved

10 ERRATA STATUS REPORT

Karen Pieper gave an overview of the bug tracking system that is being used to collect and track resolution of errata. The bug tracking and errata system can be found at <http://www.eda.org/svdb>.

11 P1364 UPDATE

a) P1364 Update

It was noted that the IEEE-SA Board has approved the PAR changes for P1364 at their 12 August 2004 meeting. The minutes of the IEEE-SA Board can be found at [Download](#). A copy of the modified PAR can be found at [Download](#).

b) P1364 Alignment Report

Victor Berman submitted an alignment report and slide presentation showing how a propose structure could look like to address the current P1800 and P1364 PARs and establish a structure for eventual convergence.

Alignment Report: <http://www.eda.org/sv-ieee1800/Meetings/2004/September/AlignmentReport.pdf>

Possible Org: <http://www.eda.org/sv-ieee1800/Meetings/2004/September/P1800 Org Slides.pdf>

There is a P1800 and P1364 projects that need to be addressed by the WG. There is a long-term goal to converge. There other structures that could better address the operation of the projects. The question is how do we address long-term and short-term?

A motion (Wolfgang, Oz) was made to direct the 1364 ETF and PTF to continue errata work, BTF to address omissions, simple extensions and encryption. The motion was unanimously approved.

Action Request: [Victor Berman] To report to the SV WG on the Data Types project in 1364 and look at what changes are required in the language to allow future support of dynamic and static types and types on wires. He will also report on the main differences between Verilog and SV data types.

12 SCHEDULE OF OUR MAJOR MILESTONES

Chair Srouji reviewed our major milestones. There are no items which are late.

Milestones: See <http://www.eda.org/sv-ieee1800/Meetings/2004/September/timeline.pdf>

13 NEW BUSINESS

None.

14 NEXT MEETING

Date: 11 October 2004

←REVISED DATE & LOCATION

Host: Cadence Design Systems
Location: Boston, MA USA

Date: 22 November 2004
Host: Intel
Location: Israel

Date: 15 December 2004
Host: Synopsys
Location: Mountain View, CA USA

Date: 26 January 2005
Host: JEITA
Location: Yokohama, Japan

15 Adjournment

A motion (Dennis, Oz) made to adjourn the meeting. The meeting adjourned at 2:40 pm CSET.