

Errata Committee Update

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Errata Tracking

- Please see the SV Operating Procedures Document

P1800 Champions Committee

- Ensure all changes across SystemVerilog and Verilog committees are aligned
- Facilitate communication across teams
- Members attend multiple committees
- Responsibilities
 - Identify issues that cross committees
 - Pro-actively communicate overlaps
 - Drive aligned solutions
 - Comment on LRM change alignment to P1800

P1800 Champions Committee

- Committee meets monthly before P1800
 - Review updates to the LRM
 - Discussion list created 1 week prior
 - Review ongoing work in the committees
 - Discuss and propose solutions crossing committee boundaries
- Meeting is open
- Only members can “veto” issues
 - Email vetos are ok

P1800 Champions Committee

- Changes can be “vetoed”
 - Editor won’t have enough meeting to make the change
 - Cross committee issues exist
 - Change is outside the PARs for the groups
 - Two proposals are inconsistent

P1800 Champion Nominees

- Karen as non-voting chair
- Francoise Martinolle (Cadence) BC, EC, CC, ETF, BTF, PTF
- Stu Sutherland (Independent) BC, EC, ETF, BTF
- Dave Rich (Mentor) BC, EC, ETF
- Brad Pierce (Synopsys) BC, EC, ETF, <BNF Expert>
- Surrendra Dudani (Synopsys) BC, EC, AC
- Bassam Tabbara (Novas) CC, AC
- Shalom Bresticker (Freescale) ETF, email on BC, EC
- Additional membership suggested
 - Need a few more users
 - Need additional coverage for SV-CC and SV-AC

Errata Review

- Friday the Champions reviewed 47 resolved issues
 - SV-BC, SV-CC, SV-EC, ETF
 - Please see handout
- All of the issues were unanimously approved except for 4 from SV-CC that were not approved
 - Could not tell when they passed
 - Format for changes were not consistent

Current Status

- 130 Open Immediate issues remain
- The Chairs believe we are still on track for the December 1 LRM Freeze