

1855 – remaining bind issues with the new 22.10

This proposal cleans up a few things that resulted from the move to the new chapter.

1. the title of 22.10) needs to be made general instead of assertion specific
2. clause 16 in section 1.6 (page 26 of draft 3) needs to be updated to exclude assertion binding and clause 22 should include it.
3. Syntax 22.3 Module item syntax replicates the bind directive from Annex A. The note that was added to Annex A needs to be replicated here too.

I think this section is now jointly owned by SV-BC so I plan to write this for SV-AC and then we can pass the approved version on to SV-BC for final approval.

Replace

Clause 16 describes immediate assertions, concurrent assertions, properties, sequences, sequence operations, multiclock sequences, clock resolution, and assertion binding.

...

Clause 22 describes how hierarchies are created in SystemVerilog using module instances and interface instances, and port connection rules. Also discusses the \$root top-level instances, nested modules, extern modules, identifier search rules, and how parameter values can be overridden.

With

Clause 16 describes immediate assertions, concurrent assertions, properties, sequences, sequence operations, multiclock sequences, and clock resolution. ~~clock resolution, and assertion binding.~~

...

Clause 22 describes how hierarchies are created in SystemVerilog using module instances and interface instances, and port connection rules. Also discusses the \$root top-level instances, nested modules, extern modules, identifier search rules, ~~and~~ how parameter values can be overridden, ~~and binding auxilliary code to scopes or instances.~~

Syntax 22-3 Replace

```
bind_directive ::=  
    bind bind_target_scope [: bind_target_instance_list] bind_instantiation ;  
    | bind bind_target_instance bind_instantiation ;
```

With

```
bind_directive ::=  
    1  
bind bind_target_scope [: bind_target_instance_list] bind_instantiation ;  
    | bind bind_target_instance bind_instantiation ;
```

Note 1: when the bind target scope or bind target instance is an interface identifier, the

bind instantiation must be an interface instantiation. (Note to editor: change the number to the appropriate footnote number)

Replace 22.10 title:

~~22.10 Binding properties to scopes or instances~~

With

22.10 Binding auxilliary code to scopes or instances