

# SystemVerilog C/C++ Committee

Status as of 02/28/03



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# SV-CC

## Focus Areas

- DirectC API.
- Assertion VPI.
- Coverage VPI.
  
- Cross-committee issues.
- LRM Editing Status.

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## Status update: DirectC

- Both SV side and C side proposals have been presented, reviewed and approved.
- All 13 **ISSUEs** are closed on DirectC.
  - > 2 C to SV interface proposals.
  - > 1 SV to C interface proposal.
  - > Other smaller proposals.
- Items delayed till SV 3.2
  - > External model
  - > Code registration

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## Status update: Assertion API

- Assertion API conforms to the Verilog Procedural Interface (VPI) naming convention and is an extension to the current VPI standards.
- Assertion API proposals have been presented, reviewed and approved.
- Asking for feedback from SV-AC.



# SV-CC

## Status update: Coverage VPI

- Coverage API conforms to the Verilog Procedural Interface (VPI) naming convention and is an extension to the current VPI standards.
- Coverage VPI proposals have been presented, reviewed and approved.
- 5 new built-in system functions.



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## Status update: Cross-committee Issues

- SV-AC:
  - > Assertion VPI.
- SV-EC:
  - > String proposal accepted.
  - > Opaque pointer proposed.
  - > Unicode rejected.
  - > Extern/export proposal.

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## Status update: LRM Editing

- DirectC C and SV side documentation available in the LRM format.
- Assertion VPI LRM is already in the SV Draft LRM.
- Coverage VPI LRM is available in the LRM format.

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## Status update: Procedural Works

- Meetings were held twice per week to accommodate more discussions and to draw closure on issues.
- One face-to-face meeting held in San Jose on 01/23/01.



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## Timeline and Open items

- Extern/export proposal.
- Checking for any inconsistency of the LRM within the team.
- What's in a name? DirectC or something else?
- Targeting 03/11/03 as the freeze date.