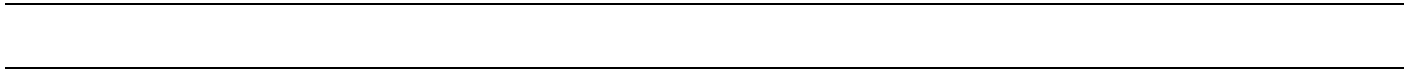


Open
 Closed
 Reopened

To open an issue any member with two seconds. A written description of the issue is required
 If there is consensus in a scheduled SV-AC meeting or issue has been voted upon
 Any SV-AC member can request an issue to be reopened must have two seconds from other members who are not part of the same company

Open issues in SV-AC v0.14 DATE: 19-Feb-03

Item #	Section	Description	Status	Raised by	Description	Resolution
1	Sem1	Sequence Implication Operator	Open	John H, Cindy E, Adrew S.	John and Surrendra submitted proposals. Surrendra's proposal is current. Issue and discussion on whether nested sequence implication is required for variable sampling.	
2	Sem2	Check construct	Open	Adam K, Bassam, Erich	Remove immediate assertions. Proposal is self evident. Prakash presented arguments for preserving.	
3	Sem3	initial and never are allowed to co-occur as directives but are not compatible	Open	Cindy, John, Erich	Do not allow intial and never to occur in same property. Proposal is self evident.	
4	Sem4	always is implicit	Open	Cindy, Erich, Adam	Require always as keyword to be explicit and that one of {always, never, initial} be required for correct syntax. Proposal is self evident	
5	Sem5	variable feature that provides for multiple assignments	Open	Erich, Cindy, John	Erich will provide proposal	
Item #	Section	Syntax				
6	Syn1	Remove Unary Delay	Open	Cindy E, 2nd Erich M and John H.	remove unary delay, leaving only binary delay. if a leading delay is needed, it can be expressed using a leading "true" and a binary delay. Proposal sent by Surrendra	
7	Syn2	Replace Seq with Sequence	Closed	Adam K, Cindy E, Bassam T	Replace seq with sequence	Replace seq with sequence. No dissent
8	Syn3	true keyword be dropped	Closed	Adam K, Cindy E, Bassam T		Remove true keyword or use 1
9	Syn4	define an implication operator where the lhs will start one cycle later than rhs	Open	Cindy E, 2nd Erich M, John H	see Cindy proposal email proposing => for next cycle implication	
10	Syn5	change delay operator semantic	Open	Cindy, 2nd Erich, Bassam	proposal by Cindy to have two different bind operators - see email (combine with issue 6 Syn1)	
11	Syn7	remove named boolean feature	open	Cindy, Erich, Adam	remove feature to define bool	
Item #	Section	DWG Issues				
12	Syn6	operator syntax	Open	Cindy, Erich, Adam	Proposals from Cindy and Roy on using various symbols to represent sequence operators	
Item #	Section	Synchronization Issues				



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